



## UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE  
United States Patent and Trademark Office  
Address: COMMISSIONER FOR PATENTS  
P.O. Box 1450  
Alexandria, Virginia 22313-1450  
www.uspto.gov



Bib Data Sheet

CONFIRMATION NO. 9323

<b>SERIAL NUMBER</b> 10/651,660	<b>FILING OR 371(c) DATE</b> 08/29/2003 <b>RULE</b>	<b>CLASS</b> 333	<b>GROUP ART UNIT</b> 2817	<b>ATTORNEY DOCKET NO.</b> P/1071-1602
<b>APPLICANTS</b> Yohei Ishikawa, Kyoto-shi, JAPAN; Hiromu Tokudera, Nagaokakyo-shi, JAPAN; Kei Matsutani, Nagaokakyo-shi, JAPAN;				
<b>** CONTINUING DATA *****</b> This application is a DIV of 10/235,469 09/05/2002 PAT 6,646,516 which is a DIV of 09/393,427 09/10/1999 PAT 6,472,960				
<b>** FOREIGN APPLICATIONS *****</b> JAPAN 10-258051 09/11/1998 JAPAN 11-212376 07/27/1999				
<b>IF REQUIRED, FOREIGN FILING LICENSE GRANTED</b> ** 11/25/2003				
Foreign Priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 (a-d) conditions <input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after met Allowance Verified and Acknowledged		STATE OR COUNTRY JAPAN	SHEETS DRAWING 11	TOTAL CLAIMS 8
Examiner's Signature _____ Initials _____		INDEPENDENT CLAIMS 2		
<b>ADDRESS</b> 000002352				
<b>TITLE</b> COMPLEX CIRCUIT BOARD, NONRECIPROCAL CIRCUIT DEVICE, RESONATOR, FILTER, DUPLEXER COMMUNICATIONS DEVICE, CIRCUIT MODULE, COMPLEX CIRCUIT BOARD MANUFACTURING METHOD, AND NONRECIPROCAL CIRCUIT DEVICE MANUFACTURING METHOD				
<b>FILING FEE RECEIVED</b> 1330	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:		<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees ( Filing ) <input type="checkbox"/> 1.17 Fees ( Processing Ext. of time ) <input type="checkbox"/> 1.18 Fees ( Issue ) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit	